



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit: 2133  
Examiner: To be assigned

**In Re PATENT APPLICATION Of:**

Applicant(s) : I-Ming LIN et al.

Serial No. : 10/614,040

Filed : July 8, 2003

For: : Signal testing of integrated circuit chip

Attorney Ref: TOP 296

## **STATUS REQUEST**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Please let us know the status of the above-identified application and when an Action may be expected. If the response to this status request is made by telephone, please ask for Tonya Harris (Docket Clerk) at extension 518.

Respectfully submitted,

May 27, 2005  
Date

*Steven M. Rabin*

SMR:klc:dj